

REMARKS

Claims 1-5 have been cancelled and new claims 6-10 added. Thus, claims 6-10 are presented for examination. Applicant respectfully requests allowance of the present application in view of the foregoing amendments.

Amendments, if any, in the International Phase are hereby incorporated by reference in their entirety in the present Preliminary Amendment.

The amendments are not made for purposes of patentability.

A substitute specification incorporating the changes in this preliminary amendment is provided with this application. No new matter has been added by way of the substitute specification.

Conclusion

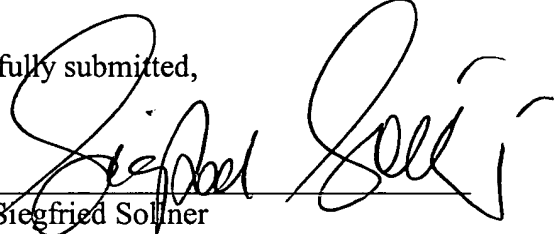
The commissioner is hereby authorized to charge any appropriate fees due in connection with this paper, including the fees specified in 37 C.F.R. §§ 1.16 (c), 1.17(a)(1) and 1.20(d), or credit any overpayments to Deposit Account No. 19-2179.

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## Description

## ARRANGEMENT WITH A PANEL OF A FLAT SCREEN

CROSS REFERENCE TO RELATED APPLICATIONS

[0001] This application claims priority to the German application No. 10347036.0, filed October 9, 2003, and to the International Application No. PCT/EP2004/011299, filed October 8, 2004 which are incorporated by reference herein in their entirety.

FIELD OF INVENTION

[0002] The invention relates to an arrangement with a panel of a flat screen, with a graphics processor, to which a digitized image signal can be supplied by an A/D converter, said image signal being generated from an analog image signal by the A/D converter. The amplification of the A/D converter can be set such that the A/D converter generates a maximum video step of the digital image signal from the highest video level of the analog image signal.

BACKGROUND OF INVENTION

[0003] Generally the contrast of a panel is factory set such that a maximum video step is generated from the highest video level, e.g. a video level of 0.7 volts, of an analog image signal, when a white image is displayed on the panel. If an 8-bit A/D converter is provided for example, the amplification is set such that said A/D converter generates a video step 255 from the highest video level of 0.7 volts. Too low a contrast level produces an image with poor contrast and gray scales are lost, as the A/D converter does not generate the maximum possible video step from a maximum video level. Too high a contrast level however means that the A/D converter outputs the maximum video step before the maximum video level is reached, causing image information to be lost.

SUMMARY OF INVENTION

[0004] The An object of the present invention is to specify an arrangement of the type mentioned above, with which contrast adjustment is simplified during a calibration phase.

[0005] This object is achieved by the claims ~~measures specified in the characterizing part of claim 1.~~

[0006] It is advantageous for influences that interfere with the contrast of an image due to ageing effects of the transparent parts of the panel, e.g. due to ageing effects of the panel glass, the LCD liquid or the diffuser films and/or polarization films, to be reduced and for it only to be possible to see the sensor via the display surface during a calibration phase. The contrast is adjusted automatically on site, e.g. when the flat screen is in use in the medical field, so there is no need for service personnel.

[0007] In one embodiment of the invention the sensor can be lowered in the direction of the panel, thereby reducing the distance between the sensor and panel, producing better measuring results.

[0008] The measuring results are also improved by providing sealing means to screen the sensor from ambient light. These sealing means, e.g. in the form of sealing lips, prevent the ambient light having an adverse effect on the measuring results.

[0009] In a further embodiment of the invention according to the measures specified in claim 4 the sensor is cleaned automatically. This largely prevents contamination of the sensor, which has an adverse effect on the measuring results, and it is also possible to increase the time intervals between sensor maintenance operations.

[00010] The sensor and the means for swiveling the sensor out are advantageously disposed in a recess in the frame, preferably the lateral frame of the flat screen, with the result that during normal operation, i.e. outside the calibration phases, the sensor and swiveling means cannot be seen by an observer.

#### BRIEF DESCRIPTION OF THE DRAWINGS

[00011] The invention, its embodiments and advantages are described in more detail below with reference to the drawing, which illustrates an exemplary embodiment of the invention, in which:

[00012] Figure 1 shows an arrangement for setting the contrast of an image that can be displayed on an LCD panel,

[00013] Figure 2 shows a diagram of the dependency of digitized voltage values on analog video voltages and

[00014] Figures 3 and 4 show a sensor disposed on a sensor support viewed from the front and side and from above.

### DETAILED DESCRIPTION OF INVENTION

[00015] In Figure 1 the reference character 1 indicates a graphics processor, to which a digitized image signal 3 can be supplied by an 8-bit A/D converter 2 to display an image on an LCD panel 4 of a flat screen. It is assumed that the digitized image signal 3 comprises video steps from 0 to 255, which the A/D converter 2 generates from an analog image signal 5 in a voltage range of 0 volts to 0.7 volts. The graphics processor 1 controls a backlight regulator 6, which adjusts the luminance of a backlight 7, to achieve essentially constant luminance conditions. To this end a backlight sensor 8 is provided, which captures the luminance of the backlight 7, which sets the backlight regulator 6 to a target luminance. The graphics processor 1 transmits the digital image signal to the panel 4 via a suitable interface 9, as a result of which the image information is displayed visually.

[00016] Reference is made below to Figure 2, in which the dependency of digitized voltage values (video steps) at the output of an 8-bit A/D converter on video voltages (analog video signal) at its input is shown. A first instance (straight line A) shows too low a contrast setting, as the A/D converter only generates a video step 192 at its output from a maximum video input voltage of 0.7 volts. This means that gray scales GS are lost compared with a correct contrast setting (straight line B). In a second instance (straight line C) the contrast setting is too high, causing the A/D converter to generate a maximum video step 255 at the output from a video voltage of as little as 0.5 volts at the input. This means that image information BI is lost compared with the correct contrast setting (straight line B).

[00017] To prevent the contrast level being too low or too high, the amplification of the 8-bit A/D converter should be set such that it generates the maximum video step 255 from the maximum video voltage of 0.7 volts when a white image is being displayed.

[00018] In order to set the amplification of the A/D converter 2 (Figure 1) automatically in a corresponding manner, during a calibration phase the graphics processor 1 reduces the amplification of the A/D converter 2 gradually from a maximum, while at the same time a

measuring unit 10 on the front face of the panel 4 scans the optical image information of a white image displayed on the panel and captures the luminance of this white image. The measuring unit 10 transmits the captured luminance to the graphics processor 1 via an A/D converter 13. If the graphics processor 1 identifies a first change in the luminance, it sets the amplification of the A/D converter 2 one step higher via a control line St, thereby terminating the amplification setting operation and therefore the contrast adjustment.

**[00019]** The white image does not have to fill the panel completely. It is sufficient to display the white image in a “calibration window”, with the option of overlaying said white image in said window, even while an image is being displayed during a normal operating phase of the flat screen.

**[00020]** Components of the measuring unit 10 include a sensor 11 and a sensor support 12, on which the sensor 11 is mounted. The sensor 11 and the sensor support 12 are disposed in a recess in the frame of the flat screen such that they cannot be seen by an observer. In order to be able to capture the luminance of the white image during the calibration phase, the sensor 11 can be swiveled out essentially parallel to the panel from a rest position into a capture position, in which the sensor 11 is positioned such that it can be seen by the observer. The sensor 11 is preferably lowered in the direction of the panel 4 during the swiveling out operation, such that the sensor 11 can capture the luminance efficiently, without touching the panel 4. After the calibration phase the sensor 11 is swiveled back into the rest position, in which the sensor 11 and the sensor support 12 are positioned in the recess in the flat screen frame, such that they cannot be seen by the observer once again.

**[00021]** So that ambient light does not interfere with the capturing of the luminance, sealing lips (not shown here) are provided, which screen the sensor 11 from the ambient light.

**[00022]** Reference is made below to Figures 3 and 4, in which a sensor 15 disposed on a sensor support 14 is shown viewed from the front (Figures 3a, 4a), the side (Figures 3b, 4b) and from above (Figures 3c, 4c). The sensor support 14 is supported in a rotatable manner and can be swiveled out by an actuator 16 and mechanical means 17. The actuator 16, e.g. an actuator by the company Nanomuscle, internet address: <http://nanomuscle.com>, is disposed together with the sensor support 14 and the sensor 15 in a recess in a frame of a flat screen, preferably on a vertical part of the frame, and is mounted on a support plate 18 covered by the

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frame and disposed parallel to the frame. As a result the support plate 18, the mechanical means 17, the actuator 16, the sensor 15 and the sensor support 14 cannot be seen by an observer of the panel in a rest position (Figure 4). In a capture position during a calibration phase (Figure 4), in which the sensor 15 captures the luminance of a panel, the sensor support 14 with the sensor 15 is swiveled out to an angle of 90 degrees, with the sensor support 14 being lowered in the direction of the panel surface (shown by lowering height H in Figures 4b, 4c) at the same time as the swiveling out operation, to position the sensor 15 nearer to the panel surface. Sealing lips (not shown here) prevent ambient light falsifying the measuring results. Once the sensor 15 is positioned, the luminance of the test image displayed on the panel in a calibration window is captured for the period of the calibration phase, with the calibration window being able to be overlaid in the normal image. The sensor 14 is swiveled into its rest position after the calibration phase.